

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:

Sitaram YADAVALLI, et al.

Serial No.: 09/531,910

Group Art Unit 2128

Filed: March 20, 2000

Examiner: H. Day

FOR: METHOD AND APPARATUS FOR MODELING AND  
CIRCUITS WITH ASYNCHRONOUS BEHAVIOR

**SUPPLEMENTAL AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Supplemental to the response filed March 3, 2005, please amend the above-identified application as follows:

*Ok to enter.  
H.D. 6/22/05*

P7896